

31036 S. PTO
09/960399



CLASS	SUBCLASS

3C1061

U.S. UTILITY PATENT APPLICATION

O.I.P.E. SCANNED <i>4km4</i> O.A. <i>1004</i>	PATENT DATE
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PATENT NUMBER

APPLICATION NO. 09/960399	CONT/PRIOR F	CLASS 257	SUBCLASS	ART UNIT 2841	EXAMINER <i>L. H.</i>
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APPLICANTS
Hiroshi Hashimoto
Koji Takahashi

TITLE
Semiconductor integrated circuit and fabrication process thereof

PTO-2040
12/99

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____. <input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED _____	
	_____ (Primary Examiner) _____ (Date)			ISSUE FEE Amount Due Date Paid	
	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER _____	

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